Objectives:
• To introduce electric circuit elements, electronic devices, and analysis of circuits containing such devices.

I WILL POST MY CLASS NOTES ON T-SQUARE IN THE RESOURCES FOLDER.

Do not purchase MyDACs. GTL has them.
Optional means first check out the book and see if you want to purchase it. But, the class notes will be adequate if you do not want to go to the expense of purchasing a book or if you do not want to carry one around.

Grading Policy
There will be four quizzes (and no separate final exam). Each quiz is worth 25 % of the course grade. Quiz dates are to be announced. The use of a calculator will NOT be allowed. I will provide formula sheets as appropriate. Any request for regrading of a quiz assignment must be made in writing within one week of getting the quiz back. There will be no make-up quizzes for any reason other than an official GT activity or an unforeseen emergency (for example, family emergency, illness). Illness is an appropriate reason for missing a quiz, but you may need to produce a note from a doctor or the dean of students representative stating that you are not able to take the quiz.

Course Grade: The course grade will be computed according to the following weights:
Each quiz (4): 25 %

Class Attendance Policy:
Attendance is required, though there is no grade. Students are expected to attend lectures and be engaged with the class. Class attendance is also the easiest way I know of to attain a first attempt at an understanding of the material. (Of course, this has to be supplemented by working homework problems and other practice.) In addition, quizzes and other in class assignments will require you to be present in class.

Quizzes
There will be four in-class quizzes tentatively on dates to be determined. Failure to take a quiz will result in a grade of zero unless you present written documentation that you have a valid excuse and that I accept the excuse (see above). Unless the excuse is related to an obviously unforeseen emergency, this documentation must be presented one week prior to the quiz or a grade of zero may result. The only excuses I will accept are official university or program activities of unforeseen emergencies. Personal travel is NOT an excuse.

The quizzes will be heavily drawn from problems given in the homeworks. Thus, mastery of homework problems is likely to translate into high quiz grades. The quizzes will be substantial and each quiz may be a fraction of the class period or the entire period as will be announced. It is your responsibility to arrive on time.

Each quiz will concentrate on material covered between specified cutoffs (TBA)—typically from the cutoff for the previous quiz. Nonetheless, knowledge of material that came before in the course will be required.
I will provide formula sheets for the quizzes. Calculators and other aids will not be permitted on the quizzes.

*Note that Quiz 4 may be scheduled during dead week. There is no separate final exam. The quizzes collectively serve as your exams.*

**Office hours**
By appointment.

**Homework**
Problems will be assigned roughly every week. Student collaboration on homework is permitted. Homework will NOT be graded, but solutions will be posted. Homework is an essential part of the course as problem solving is how most of us learn. It will also help prepare you for the quizzes.

**Student-Faculty Expectations Agreement**
A university is a place where scholars engage in free inquiry into a range of subjects. It is therefore necessary to foster an atmosphere of mutual respect, acknowledgement, and responsibility between faculty members and the student body. See [http://www.catalog.gatech.edu/rules/22/](http://www.catalog.gatech.edu/rules/22/) for an articulation of some basic expectation that you can have of me and that I have of you. In the end, simple respect for knowledge, hard work, and cordial interactions will help build the environment we seek. I also emphasize that the class provides an excellent opportunity to ask questions—both about specific subject-matter related material as well as about, well, anything. Therefore, I encourage you to remain committed to the ideals of Georgia Tech while in this class.

**Academic Conduct:**
Students in this class are expected to abide by the Georgia Tech Honor Code and avoid any instance of academic misconduct, including but not limited to:

- Possessing, using, or exchanging improperly acquired oral or written information in the preparation of a quiz or lab report.
- Submission of material that is substantially identical to that created or published by another individual, except as noted below.
- False claims of performance or work that has been submitted by the student.

Be sure to report observed instances of violations of the Honor Code! Remember, the Honor Code is about honor. Apart from devaluing your own work, the work of your classmates, and the Georgia Tech degree, Violations of the Honor Code carry significant penalties, here at Tech, and for life. Do you want to be labeled as having cheated? The trustworthiness of engineering and science (as well as the reliability and safety of products!) relies on the basic honesty of engineers and scientists. Students may work in groups of on homework assignments, though each must student make a good-faith effort to contribute to the group. Each student must also write up and turn in his/her work to integrate the knowledge.

Further information concerning materials and other aids allowed in quizzes will be given later. See the Georgia Tech Honor Code for further information or ask instructor.

**Communications:**
You are responsible for all announcements (which may include information about the problem sets, quizzes, and labs) made in class. Problem sets and quizzes will likely strongly reflect material covered in class. If you miss class, do not ask me what was covered. Handouts may also be distributed from time to time in class; it is your responsibility to obtain information from classmates if you are not present when information is given or materials are distributed. I may also email the class various information. The alias for the class corresponds to the list of those students registered for the course. Thus, if you are not getting emails, you are probably not registered. (Wait until I announce in class that I am emailing information. This will probably happen toward the end of the first week of class.) It is your responsibility to save emails.
containing information about the class.

Notes, problem sets, solutions, and various other useful information will for the most part be posted at T-Square.

The best way to contact me is via email (**put “ece3710” in the subject line!!!**), briefly immediately after class (but another class may need the room), or by appointment.

**Miscellaneous:**
Cell phones and similar devices must be turned **off** in class.

Why are you here??
• Solid-state materials are the basis of a wealth of technologies. As educated people, we should know what is going on!
• All electric, electronic, and optoelectronic devices operate through the motion of electrons (or other charged particles – ionic conductors, plasmas, gaseous electronics) in materials. As scientists and engineers, we have to know the basics.
• Circuits are everywhere! How do they work?
• The technological developments that have led to the miniaturization of devices and their extremely high speed have led to technological revolutions in your lifetime. Be part of the technology revolution!
• This may be the first time you really see the uncanny ability of mathematics to describe the physical universe. It is beautiful!

What do you have to do??
• Come to class.
• Master the concepts.
• Do the problem sets.
• Master problem solving.
• Avoid the cookbook approach to the above.
• Keep up with the material covered in lectures.
• Read the lecture notes and the book.
• Come to office hours.
• Keep an open mind.
• Ask questions.
Topical Outline (tentative: depending on pace at which material is covered):

Resistive Circuits (~3 weeks)
- Components
- Ohm's Law
- Resistors in parallel, series
- Kirchhoff's Current and Voltage Laws
- Voltage divider and current divider laws
- Thévenin Equivalent Circuits
- Superposition

Reactive Circuits (~1 week)
- Inductors and Capacitors
- Parallel and series connections of inductors and capacitors
- Transient Analysis of First-Order circuits

Frequency Analysis of Circuits (~2 weeks)
- Steady-state sinusoidal analysis and impedance
- Transfer function
- Bode plots
- Filtering

Power in AC Circuits (~0.5 week)
- Real, reactive, and apparent power
- Power factor

Fundamental Devices in Electronics (~1.5 weeks)
- Ideal diodes
- Simple piecewise linear model of diode
- MOS Field-Effect Transistors
- Operational Amplifiers

Electronic Applications (~2 weeks)
- Rectifiers
- Amplifiers
- Active Filters

Tentative Schedule: Please note that the class schedule is being finalized. Barring unforeseen circumstances, the final class schedule will be available week 1 of class (and possibly earlier). **Failure to take a quiz will result in a grade of zero** unless you present written documentation that you have a valid excuse and that I accept the excuse. Unless the excuse is related to an obviously unforeseen emergency, this documentation must be presented one week prior to the quiz or a grade of zero may result.

Quiz dates will be given in class the first week of the summer term.
### Summer 2019 Calendar

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#### 43 class days

4 final exam days

1 reading day